

CLAIM AMENDMENTS

Please amend the claims by canceling claims 63-68, 74, and 75, all without prejudice, as indicated on the following listing of all the claims in the present application after this Amendment:

1.- 62. (Cancelled)

63. -- 68. (Cancelled)

69. (Previously presented) In memory system that includes an array of non-volatile floating gate memory cells partitioned into a plurality of sectors that individually include a distinct group of said array of memory cells that are erasable together as a unit, a method of operating the memory system with a host computer, comprising:

configuring use of the memory cells within the individual sectors to provide at least distinct portions in which user data and a sector address are stored,

in response to receiving a memory address from the host computer, addressing a corresponding sector and reading the sector address from the sector address portion thereof,

if the read sector address is that of the addressed corresponding sector, sending data to the host computer that is read from the user data portion of the addressed corresponding sector, and

if the read sector address is that of a sector other than the addressed corresponding sector, addressing the other sector and sending data to the host computer that is read from the user data portion of the other sector.

70. (Previously presented) The method of claim 69, wherein the memory array is operated with the individual cells thereof being programmable into one of exactly two detectable states in order to store one bit of data per cell.

71. (Previously presented) The method of claim 69, wherein the memory array is operated with the individual cells thereof being programmable into one of more than two detectable states in order to store more than one bit of data per cell.

72. (Previously presented) The method of any one of claims 69-71, additionally comprising providing the memory array within a card that is removably connectable to the host computer system.

73. (Previously presented) The method of any one of claims 69-71, wherein the user data portion of the individual memory sectors has a capacity of 512 bytes of data.

74. - 75. (Cancelled)